

REMARKS

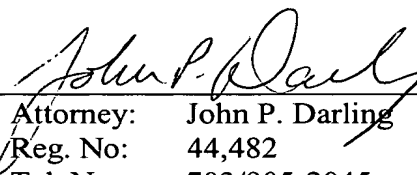
By this amendment, claims 7, 12-19, 21 and 22 have been cancelled without prejudice or disclaimer of the subject matter recited therein. Claims 1-6, 8-11 and 20 are pending.

It is respectfully submitted that the present application is in condition for allowance and a Notice to that effect is earnestly solicited. If the Examiner believes any issues remain unresolved, the Examiner is encouraged to contact the undersigned by telephone to expedite the prosecution of this application.

Respectfully submitted,

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Attachment: Appendix

10020460-121801

APPENDIX

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION:

Please amend the specification as follows:

Change the paragraph beginning on page 1, line 5 as follows:

This invention relates to a group III nitride compound semiconductor device. More particularly, it relates to an improvement in an undercoat layer for a group III nitride compound semiconductor layer such as a GaN semiconductor layer.

Change the paragraph beginning on page 1, line 22 as follows:

A substrate for a semiconductor device needs certain [characteristic] characteristics such as [(]stiffness, impact resistance, etc.[) for keeping] to maintain the function of the device. It is thought [of] that the substrate needs a thickness of 100 μm or larger in order to keep the characteristic when the substrate is formed of metal nitride.

Change the paragraph beginning on page 2, line 12 as follows:

One of the problems to be solved in the sapphire substrate is as follows. That is, the sapphire substrate is transparent, so that light of the light-emitting device to be originally taken out from an upper face of the device passes through the sapphire substrate. Hence, light emitted from the light-emitting device cannot be used effectively.

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Change the paragraph bridging on pages 2 and 3 as follows:

On the other hand, substituting an Si (silicon) substrate for the sapphire substrate may be thought of. According to the inventors' examination, it was, however, very difficult to grow a GaN semiconductor layer on the Si substrate. One [of causes] cause of the difficulty is the difference in thermal expansibility between Si and the GaN semiconductor. The linear expansion coefficient of Si is $4.7 \times 10^{-6}/K$ whereas the linear expansion coefficient of GaN is $5.59 \times 10^{-6}/K$. The former is smaller than the latter. Accordingly, if heating is performed when the GaN semiconductor layer is grown, the device is deformed so that the Si substrate is expanded while the GaN semiconductor layer side is contracted relatively. On this occasion, tensile stress is generated in the GaN semiconductor layer, so that there is a risk of occurrence of cracking as a result. Even in the case where cracking does not occur, distortion occurs in the lattice. Hence, the GaN semiconductor device cannot fulfill its original function.

END APPENDIX